



PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Attn: OIPE

Eiichi TAKAHASHI et al.

Application No.: 10/520,429

Docket No.: 122358

Filed: July 13, 2005

For: DIGITAL CIRCUIT HAVING DELAY CIRCUIT FOR ADJUSTMENT OF CLOCK
SIGNAL TIMING

REQUEST FOR CORRECTION OF PALM RECORDS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Attached is a photocopy of the original filing receipt on which errors have been corrected in red. These errors are being brought to the attention of the Patent and Trademark Office so that it may correct its records in accordance with the Preliminary Amendment filed concurrently herewith. A Supplemental Application Data Sheet is also attached.

Respectfully submitted,

James A. Oliff
Registration No. 27,075

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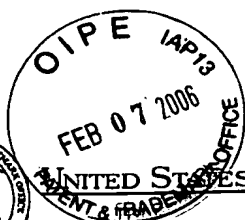
JAO:RBI/nxy

Date: February 7, 2006

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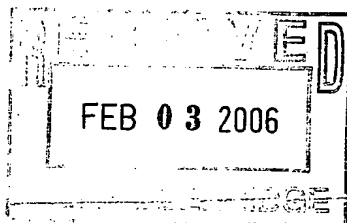
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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/520,429	07/13/2005	2631	515	122358	19	9	1

CONFIRMATION NO. 5260

25944
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FILING RECEIPT



OC000000017958383

Date Mailed: 02/01/2006

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please mail to the Commissioner for Patents P.O. Box 1450 Alexandria Va 22313-1450. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

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Power of Attorney: The patent practitioners associated with Customer Number 25944.

Domestic Priority data as claimed by applicant

This application is a 371 of PCT/JP03/08648 07/08/2003

Foreign Applications

JAPAN 2002-200467 07/09/2002

Projected Publication Date: 05/11/2006

Non-Publication Request: No

Early Publication Request: No

**** SMALL ENTITY ****

Title

~~Digital circuit having a delay circuit for clock signal timing adjustment~~
 Digital circuit having delay circuit for adjustment of clock signal timing

Preliminary Class

375

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Title 37, Code of Federal Regulations, 5.11 & 5.15

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OLIFF & BERRIDGE, PLC
ATTORNEYS AT LAW

Supplemental Application Data Sheet

Applicant Information

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Country of Residence::	Japan

Correspondence Information

Correspondence Customer Number::	25944
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Application Information

Application Type::	National Phase
Subject Matter::	Utility
CD-ROM or CD-R::	None

Title:: DIGITAL CIRCUIT HAVING DELAY
 CIRCUIT FOR ADJUSTMENT OF CLOCK
 SIGNAL TIMING

Attorney Docket Number:: 122358

Suggested Drawing Figure:: 1

Total Drawing Sheets:: 19

Small Entity:: No

Representative Information

Attorneys associated with Customer No. 25944.

Domestic Priority Information			
Application::	Continuity Type::	Parent Application::	Parent Filing Date::
This Application is a	National Stage of	PCT/JP03/08648	07/08/03
Foreign Priority Information			
Country::	Application Number::	Filing Date::	Priority Claimed::
Japan	2002-200467	07/09/02	Yes
Assignee Information			
Assignee Name::		National Institute of Advanced Industrial Science and Technology	
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